

1

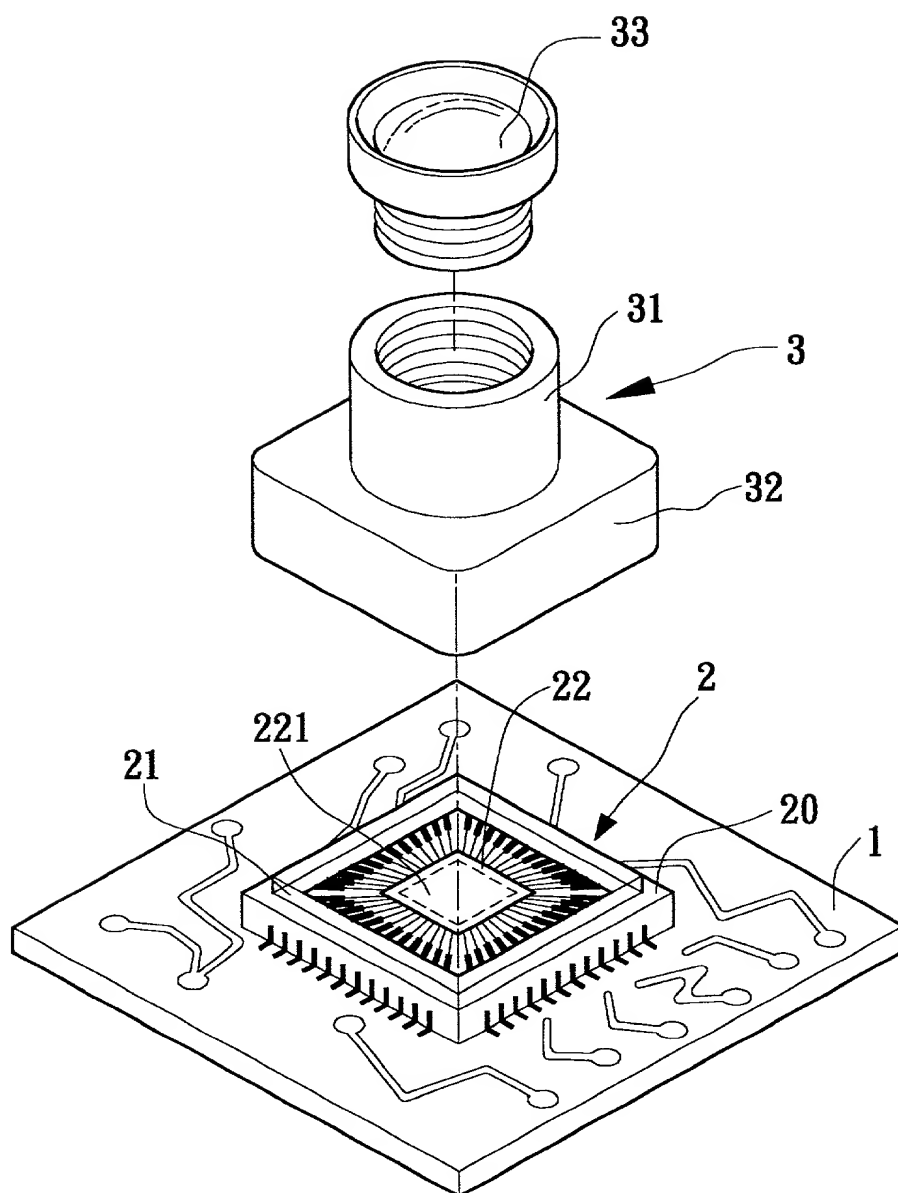


FIG. 1

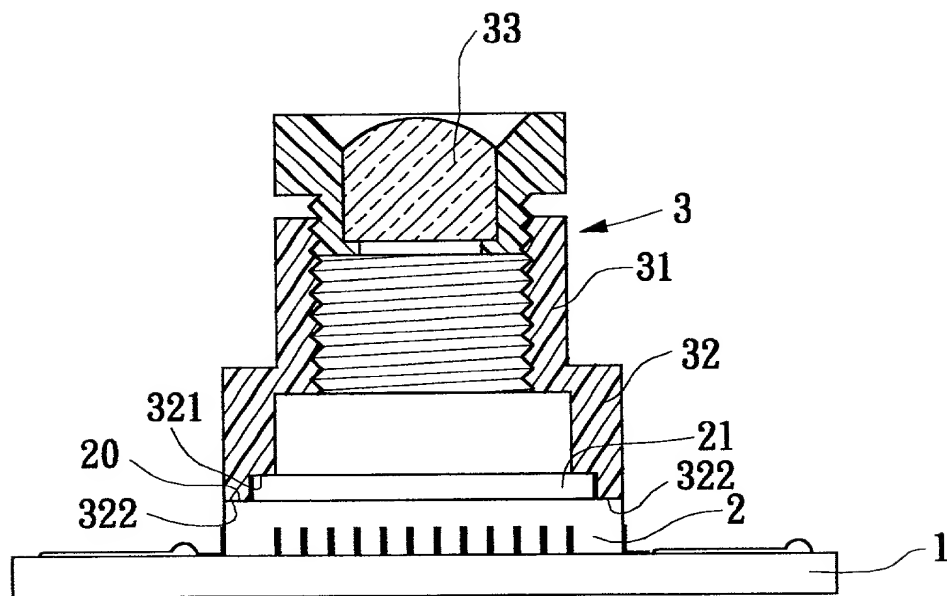


FIG. 2

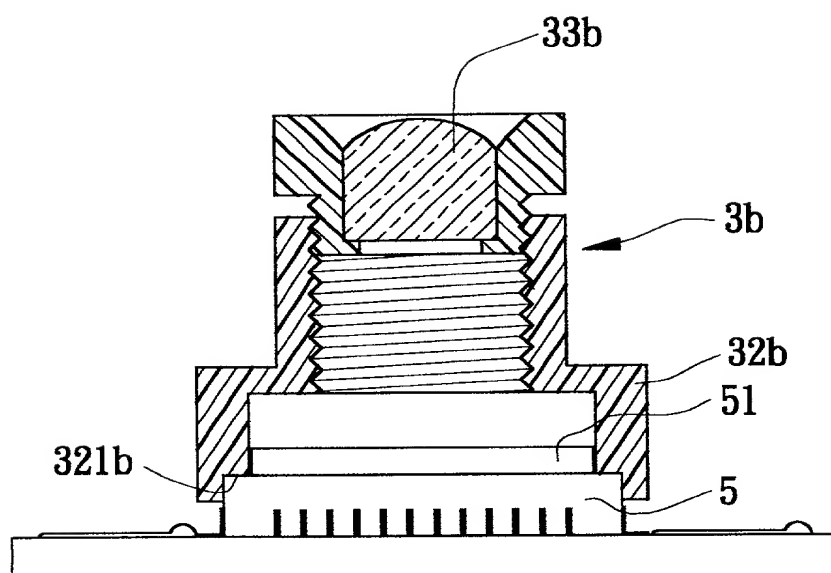


FIG. 4

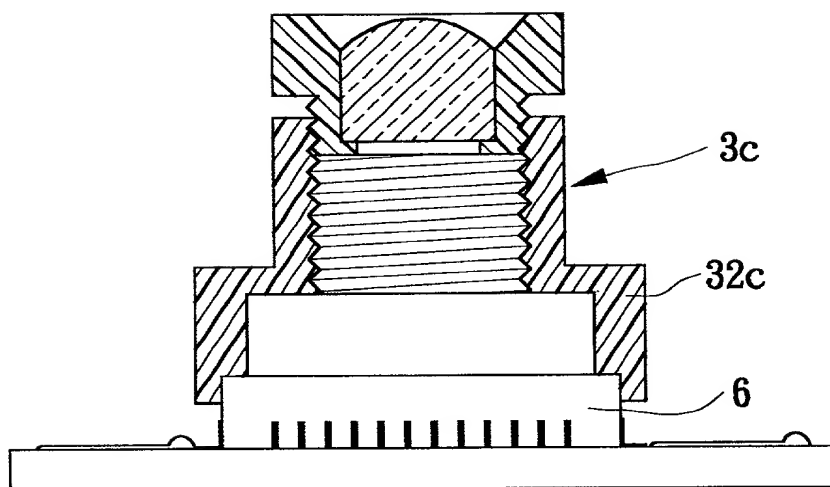
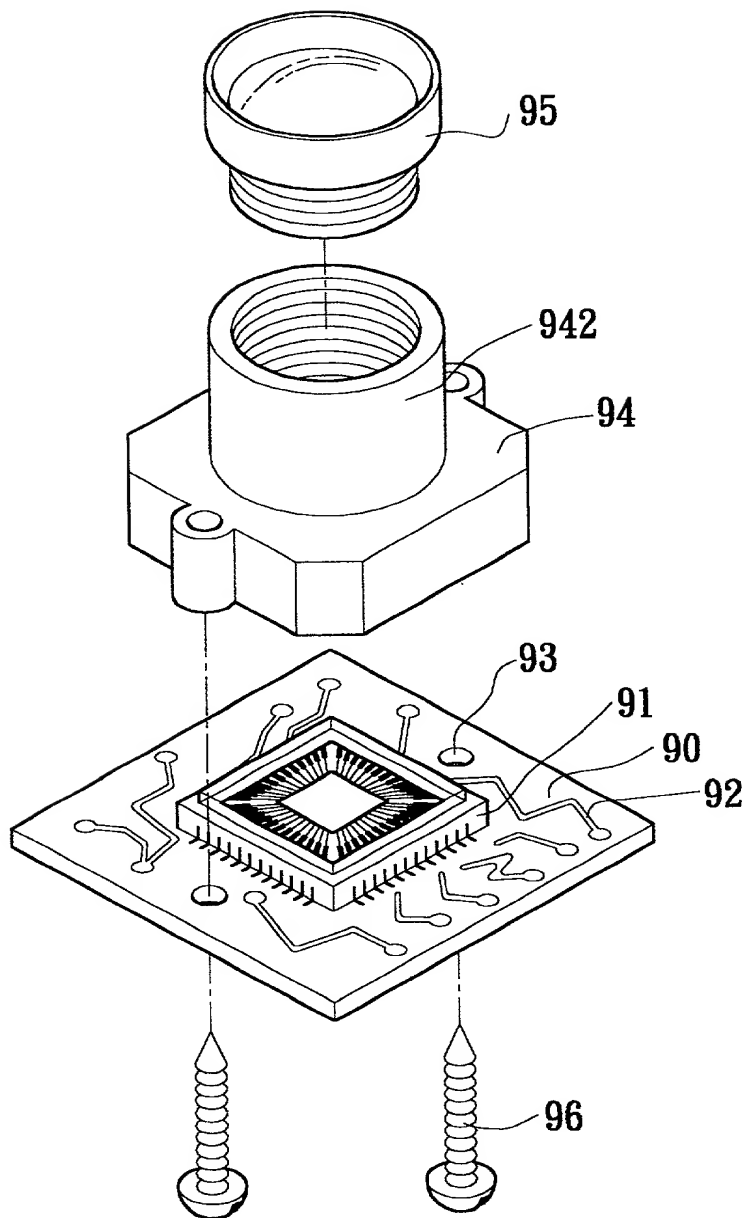
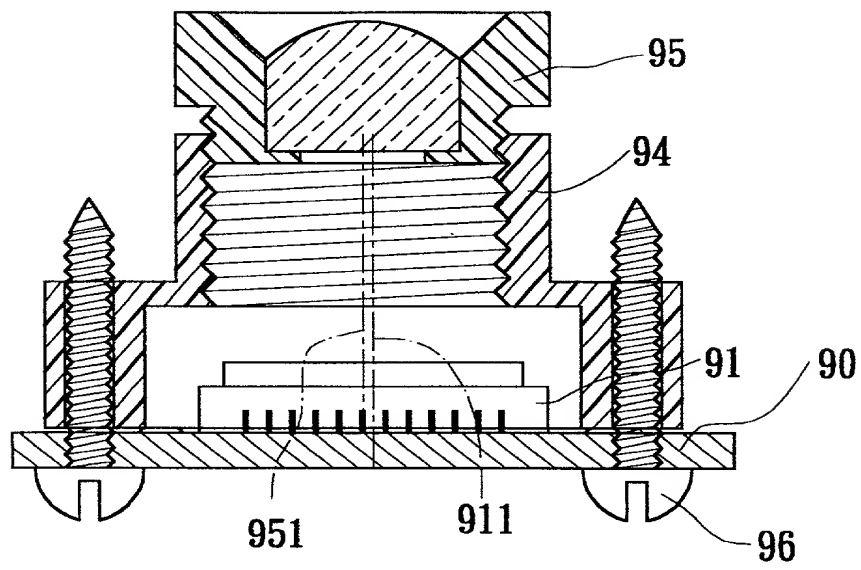


FIG. 5

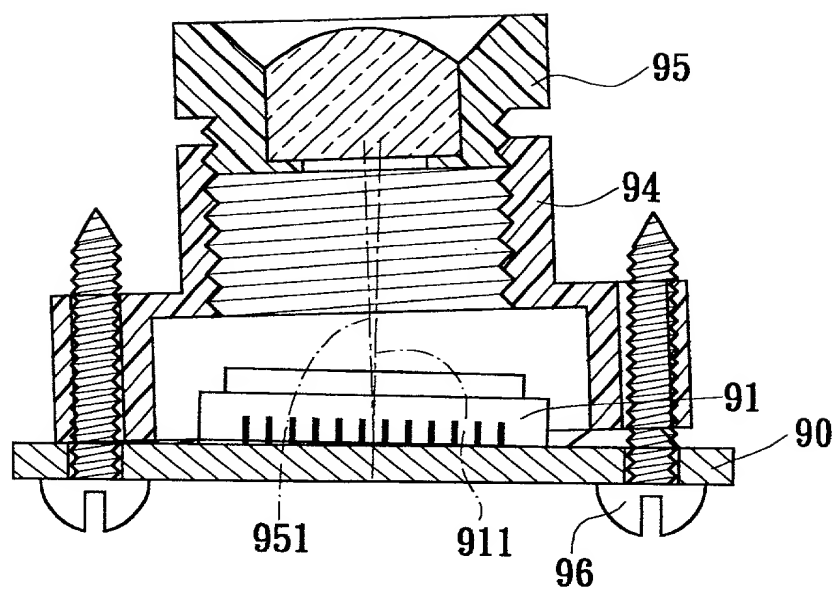
FIG. 6 is a perspective view of a prior art device. The device includes a base 90, a chip 91, and a cap 95. The chip 91 is mounted on the base 90 and has a central square region with a grid of lines. The cap 95 is positioned above the chip 91. The base 90 has four mounting holes 92, and the chip 91 has four pins 93. The cap 95 has a threaded interior 942. The base 90 is secured by two screws 96.



PRIOR ART
FIG. 6



PRIOR ART
FIG. 7



PRIOR ART
FIG. 8